

FIG 6: SCHEMATIC DIAGRAM POWERSUPPLY

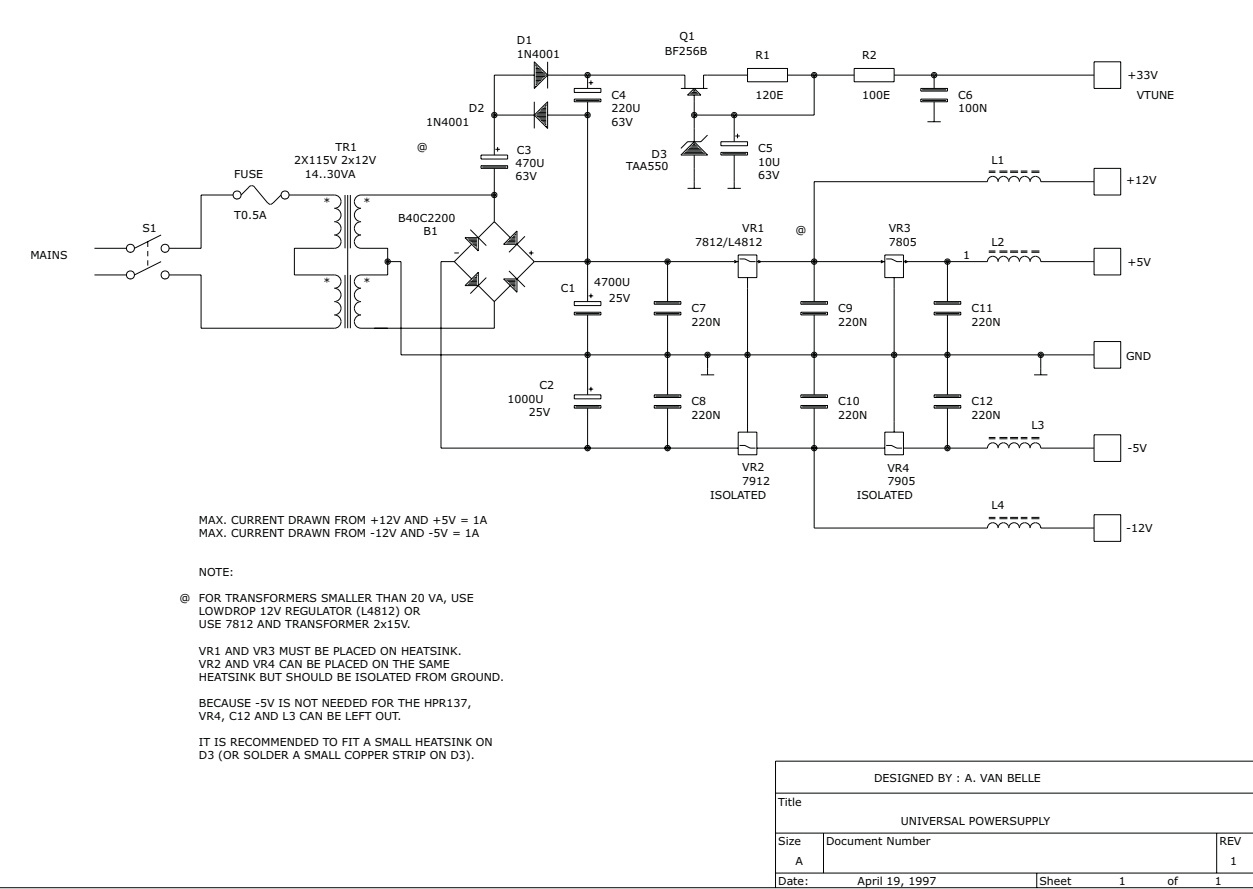


FIG 8: SCHEMATIC DIAGRAM I2C INTERFACE

